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TOP 6 WEB RESULTS out of about 6

1. [\[eros-cvs\] cvs commit: eros/src/base/sys/arch/i486 LAYOUT](#) ... @@ -23,11 +23,28 @@ Register layout for x86. This file describes both the domain * layout and the save area layout for this architecture. ... www.eros-os.org/pipermail/eros-cvs/2001-February/000319.html cached
2. [Calling Standard for Alpha Systems](#) ... Figures. 3-1 Fixed Size Stack Frame Format 3-2 Variable Size Stack Frame Format 3-3 Register Save Area Layout 3-4 Register Save Area Example 4-1 In-Memory Horned ... www.jagu.net/docs/dec/AA-PY&AB-TET1_html/TOC.html cached
3. [Jonathan Larmour - eCos anonymous CVS repository updated](#) ... Reorganized register save area layout to match changes in + hal_arch.h. + Added macros sva, lva, mvatc0, mvafc0 to transfer 64 bit items to + and from memory ... sources.redhat.com/ml/ecos-cvs/2001/msg00006.html cached
4. [Flow Control](#) ... Figure 3-3 shows the layout of the register save area. Figure 3-3: Register Save Area Layout. RSA.SAVED_RETURN is the contents of the return address register. ... www.tru64unix.compaq.com/docs/base_doc/DOCUMENTATION/V51_HTML/ARH9MBTE/FLWCNTRL.HTI cached | more results from this site
5. [PubTeX output 2000.03.10:1817 \(PDF\)](#) ... 3-6 3-3 Register Save Area Layout ... h30097.www3.hp.com/docs/base_doc/DOCUMENTATION/V50A_PDF/ARH9MATE.PDF view as html | more results from this site
6. [Hints and Tips for VSE/ESA \(PDF\)](#) ... Page 1. IBM Hints and Tips for VSE/ESA Page 2. Page 3. IBM Hints and Tips for VSE/ESA Page 4. Note | The information contained in this ... www.ibm.com/servers/eserver/zseries/os/vs/pdf3/hint6mm2.pdf view as html

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TOP 10 WEB RESULTS out of about 10

1. Sketch of S/360 Subroutines – Mark Smotherman [\(PDF\)](#)
... instruction set extensions along the way: S370, S370-XA, ESA/370, and ESA/390; ... pseudo-op to tell assembler to use r12 at r13,savearea+4 ! store address of ...
www.cs.clemson.edu/~mark/subroutines/s360.html cached
2. Dave's S/390® Assembler FAQ page [\(PDF\)](#)
... Most of the ESA/390 instruction set and many of the IBM OS/390 and DFSMSdfp services and ... such as what to do if your caller provides a 72-byte savearea, and you ...
www.planetmvs.com/hlasmv/s390faq.html cached
3. [http://www.cs.niu.edu/csci/567/ho/pcrtn.ps \(POSTSCRIPT\)](http://www.cs.niu.edu/csci/567/ho/pcrtn.ps) [\(POSTSCRIPT\)](#)
... contain length! Macros/SVCs require R13 to point to a savearea. ... 1991 "Enterprise Systems Architecture/390"; Jeff Frye; IBM Corporation; "ESA/390 Principles of ...
www.cs.niu.edu/csci/567/ho/pcrtn.ps [view as html](#) | more results from this site
4. [Zee Guide to z/Architecture Zee Guide to z/Architecture \(PDF\)](#) [\(PDF\)](#)
... In S390 mode and a SIGP is issued to set the architecture mode to z/Architecture Bits 56-63 of parameter register specify mode: X'00' specifies ESA/390 mode X ...
docs.share.org/proceedings/sh100/s2818.pdf [view as html](#)
5. <http://neuro2.med.uni-magdeburg.de/~markgraf/mys/hercules-2.12n/vector.c> [\(C\)](#)
... S370 & ESA/370) /* SA22-7207-00 Vector Operations (ESA/390) */ 28/05 ...
1) b= 0x0000FFFF; regs->GR_L(gri1 + 1) |= n1 << 16; /* Update savearea address */ regs ...
[www.neuro2.med.uni-magdeburg.de/~markgraf/mys/hercules-2.12n/vector.c](http://neuro2.med.uni-magdeburg.de/~markgraf/mys/hercules-2.12n/vector.c) cached
6. [Getting Out of the Dumps with the VM Dump Tool \(PDF\)](#) [\(PDF\)](#)
... pfrvcnfg +020C PFXFTO 7D7FF000 System frame table origin Note for ESA/390 (not ESAME ... defined in CPEXSCHC (00) Bits defined in CPEXCALC (80) 80 Savearea in use ...
www.share.org/proceedings/sh98/datas/S9369.PDF [view as html](#)
7. [TCP/IP for VSE/ESA IBM Program Setup and Supplementary ... \(PDF\)](#) [\(PDF\)](#)
... The following terms are trademarks of the IBM Corporation in the United States or other countries or both: ACF/VTAM AIX AS/400 CICS CICS/VSE ESA/390 ESCON ES ...
www.ibm.com/servers/eserver/zseries/os/vse/pdf2/vse261/testce21.pdf [view as html](#)
8. [Programmer's Guide \(PDF\)](#) [\(PDF\)](#)

- Page 1. High Level Assembler for MVS & VM & VSE IBM Programmer's Guide Release 3 SC20-4941-02 Page 2. Page 3. High Level Assembler ...
www.math.uni.lodz.pl/~miodek/OS/390/asmp1002.pdf [view as html](#)
9. [CICS Transaction Server for z/OS BigCommareas: How to bypass the ... \(PDF\)](#) [\(PDF\)](#)
... MVS DFSMSdfp DFSMSdss DFSMSshm DFSORT DXT eNetwork Enterprise Systems Architecture/370 Enterprise Systems Architecture/390 ESA/390 ESA/370 ESA/390 ES/3900 ...
www-3.ibm.com/software/tiv/cics/library/whitepapers/BigCommarea.pdf [view as html](#)

10. [VSE Jun title copy \(PDF\)](#) [\(PDF\)](#)
... B232RDR CSECT USING * ,R15 ESTABLISH ADDRESSABILITY STM R14,R12,12(R13) SAVE CALLER'S REGISTERS LA R10,SAVEAREA ADDRESS OWN SAVE AREA ST R10,8(R13) SAVE ...
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1. [Linkage](#) ... Word 16. Contents of register 10. Word 17. Contents of register 11. Word 18. Contents of register 12. Save areas are chained in a double-linked list. ... hansen-family.com/mvs/Linkage.htm cached | more results from this site
2. [Bixoft Course 2 - High Level Assembler: Interfaces](#) ... Program linkages and save areas; Obtaining PARM data from the EXEC statement; OSAM macros and processing; SYNAD routines; Subroutines and the Linkage Editor; The ... www.bixoft.nl/english/course2.htm cached | more results from this site
3. [User's Guide - Assembler-Level Subroutine Linkage Conventions](#) ... The linkage convention allows for argument passing and return values to be in storage that is used to hold local storage, register save areas, parameter lists ... hpfc.nersc.gov/vendor_docs/bmvl/html/UG79.HTM cached
4. [COBOL for MVS Dumps and Debugging](#) ... Module linkage CALL: passing parameters; ENTRY/GOBACK; CANCEL; flow of execution; save areas; using save areas; locating items in a called module. ... www.rsm.co.uk/courses/appd/cd.htm cached | more results from this site
5. [Subroutine Linkage \(PDF\)](#) ... I look at one example here: the MIPS R2000 subroutine linkage conventions, but ... Save areas? When we talk about saving and restoring registers, what are we ... www.cs.ualberta.ca/~macg/C229/Notes/Lec12.pdf view as html | more results from this site
6. [Excalibur Training Services](#) ... Program Linkage, Conventions; The calling program; called program; Passing parameters; Save areas, Input/Output, Macros; Open, Close, Get, Put, DCB, ... www.bntinternet.com/~excalibur/training/assembler_programming.htm cached | more results from this site
7. [Tips & Newsletters-Search390.com, a TechTarget Web site](#) ... Let's leave out STACK instructions and XPLINK linkage for now. We're just concerned with ages old MVS save area chaining. That's where save areas contain LSA ... search390.techtarget.com/tip/1_289483.sid10_gc882782.00.html cached
8. [Xephon Archives](#) ... www.xephon.com/xarcinfoframe.php/a012a02 cached | more results from this site
9. [Don't Let the DUMPS Get You Down](#) ... ABEND -REGS - Contents of general registers at entry to ABEND - SA - Requests save area linkage information and backward trace of save areas - SAH -

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Yahoo! Search Results for "save areas" linkage

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Requests ...
support.z.com/technologies/databasem/databasem_ideal/25181.html cached

10. [C411 Outline](#) ... Program Linkages Control Sections Save Areas Addressability Return Codes Typical Linkages SAVE ... Subroutines and the Linkage Editor CSECTs and the Linkage Editor ... www.trainersfriend.com/C4110.htm cached | more results from this site
11. <http://www.fsa.usda.gov/debes/MDBC9007.txt> ... 01610054 * 01620054 * COUNTERS AND SAVE AREAS *01630054 * 01640054 ... 01720054 * 01730054 * LINKAGE SECTION *01740054 * 01750054 ... www.fsa.usda.gov/debes/MDBC9007.txt cached
12. [MVS 3](#) ... SA or SAH, Save area linkage information and backward trace of save areas. (This option is selected by ALLPDATA). SAH requests only ... www.tommysprinkle.com/mvs/feabd0.htm cached
13. [Dave's S/390 Assembler FAQ - Top Ten](#) ... Scott sent me this one: Program uses R2 as a base or linkage register; You ... the "Save Area Trace" portion of the SYSUDUMP output (if you chain your Save Areas). ... www.planetrmvs.com/hlasm/topten.html cached
14. [IBM IT Education Services - United Kingdom - ASM02GB Advanced](#) ... www.ibm.com/services/learning/uk/ita-lts/rsl/ExtCourseNr/ASM02GB cached | more results from this site
15. [Subprograms \(MICROSOFT POWERPOINT\)](#) ... Must retain status in static location. Subprogram Linkage. Finding Entry Point: ... How does routine return to Caller? Static Save Areas. STM 14,12,12(13). ... www.personal.psu.edu/faculty/w/wlg114/CSE428/Subprograms.ppt view as html
16. [Mainframe Week Issue 26, 3 July 2002, Analysing DSNC abends](#) ... which SQL statement caused the error, we have to go through the save areas until we ... by COBOL to address the fields in the working storage and linkage section. ... www.mainframeweek.com/journals/articles/0026/Analysing+DSNC+abends.cached
17. [CICS V2 TO V4 FOR APPLICATIONS PERSONNEL \(PDF\)](#) ... Fax: 01273 731345 c c o o o u u r r s s e e f f a a c c t t s s h h e e e e l l t t Calls and linkage Conventions; save areas; parameters; addressing. ... www.circle-group.com/training/as_2003.pdf view as html
18. [8.16 Restrictions](#) ... of COBOL working storage, USER GETMAIN areas, CICS GETMAIN areas, register save areas, etc. ... Examples: Use COBOL LINKAGE SECTION instead of WORKING STORAGE. ... www.state.sd.us/standards/Section%208-16.htm cached
19. [Sketch of S/360 Subroutines – Mark Smotherman](#) ... byte 24: contents of r1 ! ... 1 byte 68: contents of r12. Further discussion. You must

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dynamically allocates save areas to support recursion. ... www.cs.clemson.edu/~mark/subroutines/s360.html cached

20. [Using Assembler Language at the NIH Computer Center - September](#) ... A detailed description of OS/VS Binder conventions, save areas, and parameter lists may be ... registers 0,1,13,14, and 15 are known as the linkage registers and ... datacenter.cit.nih.gov/assem/assem-07.htm cached

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- SC26-4916 - Program Management. Major Topics include: Program linkages and save areas; Obtaining PARM data from the EXEC statement; ... [www.bixoft.nl/english/course2.htm](#) cached | more results from this site
- Ask the Experts - Search390.com, presented by TechTarget [more results from this site](#)
- Linkage [more results from this site](#)
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- Communication with Assembler Programs : Calling aC Program from ...** [Calling aC main Function from Assembler via SMAINO MAINASM CSECT STM 14,12,12\(13\) standard OS entry linkage BALR 9,0 USING *,9 LR 14,13 LA 13,SAVEAREA ST 14,4 ... support.sas.com/documents/onlinedocs/sasc/doc/clug/zasscp.htm cached | more results from this site](#)
- Lecture 11: Cache and Memory (MICROSOFT POWERPOINT)** [... Preview Potential Perilous Process Problems: Inter-process communication, scheduling, deadlock, ... buffer, 'I', .sidnt_stub: sw \[savearea\], R0, sw \[savearea\]+4, R1, ... www.cs.gatech.edu/classes/cs3431_99_winter/lectures/990112-proc.ppt view as html | more results from this site](#)
- Xephon Archives** [... LR R11,R15 LOAD BASE ADDRESS. L R3,0\(R1\) GET COMMUNICATION AREA ADDRESS, GETMAIN R,LV=AREALN GETMAIN A REGISTER SAVEAREA. USING WORKAREA,R13 SET AREA BASE. ... www.xephon.com/farcinframe.php/n003a06 cached | more results from this site](#)
- http://www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt (MICROSOFT POWERPOINT)** [... void ExitProcess\(int exitCode\);. Interprocess communication \(IPC\) related. Note SaveArea, ProcessDescriptor, & interrupt vector pointers. ... www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt view as html](#)
- Mainframe Week - Code db2** [... _RUN EXTERNAL ROUTINES ** CLOSE - CLOSE A DATASET ** DCB - DATA COMMUNICATION BLOCK ** GET ... ENTRY POINT USING \(258,R12 USE R4 AS BASE XC SAVEAREA\(72\),SAVEAREA ... www.mainframeweek.com/codes/showcode.php?0015/nw15d23.txt cached | more results from this site](#)
- Using the 3494 Tape Library under VSE/ESA** [... Figure 3. Communication paths *--- VSE/ESA ... R10,TCATEG LA R13,SAVEAREA USING IJLBSER ... www.ibm.com/servers/eserver/zseries/os/vse/support/et/vse5sup.htm cached | more results from this site](#)
- Parallel Systems (MICROSOFT POWERPOINT)** [... Inter-thread communication and synchronization is very fast. ... int timeLeft; int nextThread, prevThread; int pid; ThreadState state; SaveArea sa; }; struct ... www.cs.unm.edu/~crowley/osbook/slides/chap06.ppt view as html](#)

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- <http://www.freeusp.org/Arc0/Data/sparc/mowwd> [... 00003800 * 00003900 R0 EQU 0 SUBROUTINE COMMUNICATION 00004100 R1 ... R12,12\(R13\) SAVE REGISTERS 00006800 LA R12,SAVEAREA POINTER TO ... www.freeusp.org/Arc0/Data/sparc/mowwd cached | more results from this site](#)
- Tips & Newsletters-Search390.com, a TechTarget Web site** [... Change routing Information \(IP address?\) for priority communication. ... Store ANCHOR into CVTUSER or into first word of JOB STEP TCB's savearea. ... search390.techtarget.com/tip/1,289483,sid10_gc915653,00.html cached](#)
- Bixoft - Macro library - Bixoft eXtended Assembly language** [... OCUB, MAPOUCB, ResOurces manager User Control Block, PCCA, MAPPCCA, Physical Configuration Communication Area. ... SAVEAREA, MAPSAVE, Standard SAVE AREA. ... www.bixoft.nl/engls/h/maclib.htm cached](#)
- <http://www.itc.virginia.edu/~drs8h/articles/cpuupd1.txt> [... ESTABLISH USING CPUSCAN,R12 ADDRESSABILITY LR R12,R15 LA R11,SAVEAREA ST R11 ... ENTRY FOR ASCBS *** OPERCOMM LA R9,COMFLD SET UP OPER COMMUNICATION AREA EXTRACT ... www.itc.virginia.edu/~drs8h/articles/cpuupd1.txt cached](#)
- VSE Doc title (PDF)** [... SAVEAREA DS 9D EJECT ***** CROSS-PARTITION COMMUNICATION CONTROL BLOCK \(MODIFIED ... www.bmboyle.com/bminfo/pdfmags/dec_97.pdf view as html | more results from this site](#)
- Modern CICS Application Development: The Big Picture (PDF)** [... JSP's Java Servlet EJB MQSeries Web Servers App Servers Databases Transactional Systems Communication Enterprise Applications Page 4. ... www-3.ibm.com/software/tpf/svccon2001/cicscd/KeyView.pdf view as html | more results from this site](#)
- <http://www.cbtape.org/uclamail/pcprog.txt> [... Communication link speed is also assumed to be fast enough to avoid requiring special ... rest of the registers are undefined, R13 does NOT point to a savearea. ... www.cbtape.org/uclamail/pcprog.txt cached](#)
- Introduction To Programming In An IBM Environment (PDF)** [... AND TRANSACTIONS Multiregion Operation Intersystem Communication Transaction ID ... the Base and Displacement Addressing mechanism, and Register Savearea chaining. ... www.dld.com/pdf/datalainpdfs/intro.pdf view as html](#)
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1. http://www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt (MICROSOFT POWERPOINT) ... reschedule a process. Save the context of current process, if there was one (page133). SaveArea *savearea = &(p[<current_process>].sa); asm { ... www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt view as html | more results from this site }
2. <http://www.sinenomine.net/downloads/openafs-1.0.2-s390.diff> ... else savecontext(&Dispatcher, &temp -> context), &temp.WPANCHOR ... 1 ***** 85,87 ***** #if defined(DEBUG) { int i, *ptr = (int*)savearea->setup_buffer ... www.sinenomine.net/downloads/openafs-1.0.2-s390.diff cached }
3. http://www.mit.edu/afs/athena/project/net_dev/events/lwp/lwp.c ... int (*ep0), struct lwp_context *savearea, char *sp); Stub for Assembler routine that will save the current SP value in the passed context savearea and call ... www.mit.edu/afs/athena/project/net_dev/events/lwp/lwp.c cached
4. Python: module rox_saving ... A SaveArea contains the widgets used in a save box ... drag_begin(self, drag_box, context) drag_data_get(self, widget, context, selection_data, info, time) drag_end ... rox.sourceforge.net/rox_lib_docs/rox/saving.html cached
5. http://openbsd.secups.org/src/usr.sbin/afs/src/lwp/lwp_asm.c ... Stub for Assembler routine that will * save the current SP value in the passed * context savearea and call the function * whose entry point is in ep. ... openbsd.secups.org/src/usr.sbin/afs/src/lwp/lwp_asm.c cached
6. Re: Porting Arta to Linux/390 ... void savecontext(void (*)void, struct savearea *context, void *new_sp); void returnto(struct savearea *context); savecontext saves all necessary processor ... www.stacken.kth.se/lists/arta-drivers/2000-07/msg00034.html cached | more results from this site
7. er.dublincore.org/_TView ... id></p> + <% *table> + <% } 1. er/src/context/spl/login ... String erUser = getCookie(request, "erUser"); + String saveArea = null; + String ... er.dublincore.org/er-cvs/msg0125.html cached
8. Lecture 1: Course Introduction and Overview (MICROSOFT POWERPOINT) ... 2. run handler. Mackenzie Spring'03. Context Switch in Handler. void xxx_handler (void) { /* ... need to switch */ processes? * change 'savearea' */. }. xxx_stub: ... www.cc.gatech.edu/classes/AY2003/cs2200_spring/Slides/Lec10-proc.ppt view as html | more results from this site
9. port-mac68k: Re: Kernel ... f_regs.sizeof(flt->_regs)-2NPW); /* Restore the original FP context */ @@ - 372,15 ... the user SP movl a0,sp@(_FR_SP) | in the savearea - movl #SYS_sigreturn,sp ... mail-index.netbsd.org/port-mac68k/1998/09/24/0021.html cached
10. <http://www.iuassn.com/UCL/viewdb01.txt> ... LINKAGE DSECT DS H PARMCVN DS CL3 VS24BIT DSECT SAVEAREA DS 18F ... SLACK DS CL42 BUFACVN DS CL3 BUFFHEAD ORG BUFFER /* Header record context map --- DS ... www.iuassn.com/UCL/viewdb01.txt cached
11. <http://lists.opensfs.org/pipermail/opensfs-devel/2003-May/004317.html> cached | more results from this site
12. <http://lists.opensfs.org/pipermail/eros-cvs/2001-June/000557.html> cached | more results from this site
13. <http://www.zetetic.com/b/papers/805/task.pdf> view as html
14. http://www.abisource.com/doxygen/ispell_checker_bh-source.html cached
15. <http://cvx.pld.org.pl/ispell/Attic/correct.c?rev=1.64> ... were formerly global functions 00044 // passed a context structure pointer ... P ((char_t *)word, char_t *pattern, 00067 ichar_t savearea[MAX_CAPS] [INPUTWORDLEN] ... www.rtems.com/rtems/mailListArchives/items-users/2000/october/msg00055.html cached
16. <http://www.kld.dk/pub/gnu/gnu-utils/gnu/ispell-3.120/correct.c> ... Some are really out of context, so it is best to ignore them ... timebaseData record nextTickH ds.l1 nextTickL ds.l1 tickCount.ds.l1 endr saveArea record SavedSP ... www.rtems.com/rtems/mailListArchives/items-users/2000/october/msg00055.html cached
17. <http://www.kld.dk/pub/gnu/gnu-utils/gnu/ispell-3.120/correct.c> ... firstdir, struct flagent * pfirst, struct flagent * sufent, ichar_t savearea [MAX_CAPS][INPUTWORDLEN] ... blank line between the * columns and the context, plus a ... www.kld.dk/pub/gnu/gnu-utils/gnu/ispell-3.120/correct.c cached | more results from this site

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18. [port-mac68k: Re: Kernel](http://port-mac68k:Re: Kernel) ... f_REGS.sizeof(flt->_REGS)-2NPW); /* Restore the original FP context */ @@ - 372,15 ... the user SP movl a0,sp@(_FR_SP) | in the savearea - movl #SYS_sigreturn,sp ... mail-index.netbsd.org/port-mac68k/1998/09/24/0021.html cached
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... (P18271), IBM, AQ70049 detail. DKNMREAD, PCTL abends subpass. . This APAR corrects a problem with DKNPLINK not creating its internal saveareas in the MVS standard. ...
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6. <http://wuarchive.wustl.edu/mirrors/NetBSD/NetBSD-current/xsrc/xfree/cx/programs/Xserver/mi/mbank.c> ...
... pScreen = pPixmap->drawable.pScreen; RegionRec rgnClipped; int i;
SCREEN_INIT; SCREEN_SAVE; SCREEN_UNWRAP
(BackingStoreFuncs.SaveAreas); if (IS_BANKED(pWin ...) ...
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... There is a technique to avoid both the HLL callbacks and the staging of data. Yes, it's the old "seesaw" trick with saveareas. This ...
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... (Het is overigens niet eenvoudig dit exact vast te leggen. Bijvoorbeeld, sommige saveareas in de stack kunnen wel mogelijk veranderen.). ...
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15. <http://members.tripod.com/~billialonde/asm/asmps.txt> ...
... AS BASIC USING ASMPS,R12 PROGRAM ADDRESSABILITY LA R0,WORKSIZE SET WORKAREA SIZE GETMAIN R,L,V=(0) AND GET IT ST R13,4 (R1) * ST R18,R13) * CHAIN SAVEAREAS LR R13 ...
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17. [MainFrameForum - U4083 - dsa corrupted with LE-enabled ASM - ...](#) ...
... The easiest and fastest way to find out the 'wrong' pgm is, to use a debugger, which is able to watch all content-change of that saveareas. ...
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Within the architecture of MVT, MVS, OS/390 and z/OS, certain conventions have been established regarding the use of registers. These conventions will have been followed when you, the problem programmer, receive control from the operating system. They also should be followed for any routines, which you call, or for communications with the system (e.g. system macro calls, SVC's, returning control, etc.). Following these conventions will make your code easier for someone else to follow. Certain debugging aids are also available for those who adhere to standard conventions. In general, unless there is a STRONG reason to deviate, these conventions should be employed. The Operating System conventions are:

Return Register

Register 14 is called the "return register" and contains the address to which this routine is to return upon exit.

Entry Point Register

Register 15 is called the "entry point register" and contains the address through which this routine was entered. Note that temporary addressability may be established by using the following at the start of your routine:

```
USING ENTRYPOINT,R15
```

If this routine calls no other routines, register 15 may be used as a permanent base register. If this routine calls any other routine (including operating systems service) however, register 15 will be changed, and should not be used as a permanent base register. In this case, the sequence

```
LR      BASEREG,R15
USING  ENTRYPOINT,BASEREG
```

(Where BASEREG is any of registers 2-13) may be used to establish permanent addressability.

On return, register 15 may be used to return a code to indicate normal or error return. One frequently used technique is to set register 15 to zero on a normal return and set it to non-zero if some error condition occurred prior to return.

Register 13 is called the "save area register". It contains the address of an 18 full word area (on a full word boundary) within the calling routine. The routine called will use this area to save the contents of registers, to be able to return the registers intact to the calling program. This save area has a set format as follows:

Save Area Word	Description
Word 1	Used by PL/I and FORTRAN
Word 2	Address of the save area used by the calling program
Word 3	Address of the save area set up by the called program
Word 4	Address to which to return (register 14)
Word 5	Address of the entry point (register 15)
Word 6	Contents of register 0
Word 7	Contents of register 1
Word 8	Contents of register 2
Word 9	Contents of register 3
Word 10	Contents of register 4
Word 11	Contents of register 5
Word 12	Contents of register 6
Word 13	Contents of register 7
Word 14	Contents of register 8
Word 15	Contents of register 9
Word 16	Contents of register 10
Word 17	Contents of register 11
Word 18	Contents of register 12

Save areas are chained in a double-linked list. At any low-level routine, by tracing back through a chain of save areas links, one can eventually return to the system at the original point of call.

When your routine is entered, first you should save registers and then establish and like your own save area.

```
STM   R14,R12,12(R13)  SAVE REGS 14,15 AND 0-12
LA    R5,MYSAVE        GET ADDR OF MY SAVE AREA
ST    R58(R13)         LINK CALLING PGM S.A. TO MINE
ST    R13,4(R5)         LINK MY S.A. TO CALLING PGM'S
LR    R13,R5            POINT TO MY SAVE AREA NOW
```

On return

```
XR    R15,R15          SET ZERO RETURN CODE
L     R13,4(R13)        RETRIEVE ADDR OF CALLING PGM'S
LM    R14,R12,12(R13)  SAVE AREA
BR    R14               RESTORE REGISTERS
                      RETURN TO CALLER
```

Return Value Register

Register 0 is used to return a single result from some process (as in a FORTRAN function subprogram). Note: although you will probably not use this convention much, it is heavily used by the operating system. Register 0 cannot be guaranteed to be intact after executing some call to the system, as a system MACRO, or an SVC.

Argument List

Register 1 is the pointer to an argument list. It contains the address of the first of one or more full word entries (on consecutive full word boundaries). These entries are the addresses of arguments to be used by the calling routine.

If there are an indefinite number of arguments, (as with a routine which would accept one, two, or any number of arguments), the first bit of the last address is set to a 1. (this bit will not interfere with 24-bit addressing. This cannot be used when running in 31-bit mode.

The following example illustrates how to use the address list passed through register 1.

LA	R1,ARGLIST	GET ARGUMENT LIST ADDR
L	R15,V(CALLRTN)	GET ADDRESS TO CALL
BALR	R14,R15	CALL THE ROUTINE
ARGLIST	DC A(ARG1)	
	DC A(ARG2)	
	DC X'80',AL3(ARG3)	
CALLRTN	CSECT	
	L R2,0(R1)	GET ADDR OF NEXT ARG
	LTR R1,R1	LAST ARG. IN LIST?
	BM RETURN	IF YES, RETURN
	LA R14,0(R1)	ELSE GET ADDR OF NEXT ARG

When a programmer receives control from the system, information from the PARM field of his/her EXEC card is passed via register 1. Register 1 points to a full word of storage. Bit 0 of this full word is set to 1 (to indicate the last-only-argument of the list). This full word contains the address of a half word. The half word is a count of the number of characters in the PARM field message, and these characters follow immediately after the half word count field. The contents of the half word may be picked up to use as a length count in an execute instruction, and the address of the half word may be used as a base to move the information characters of the PARM field.

Save Area Register

```
MYSAVE DC 18F'0          MY SAVE AREA
```

A calling program is known as a "higher routine", and the routine called is the "lower routine". Register 13 is always to point to an area whose contents may be destroyed.

An exception to the requirement that a routine must always establish a save area is that the lowest-level routine (the one which call no others) need not set up a save area. The reason for this is the save area is for the use of any called routines, but that the lowest-level routine will have no called routines.

Save Area Naming

The name convention is a means of having the EBCDIC form of the name of a routine appear at certain key places on dumps. To use this convention, the first four bytes of a routine must be a branch, on register 15 as a base register, which passed over a series of bytes. These bytes contain the EBCDIC form of the name of a routine, and also a length code for this name area. This example shows how to code a name field.

```
NAME   CSECT
      B      M+1+4,(R15)
      DC    X'M'
      DC    CLM'NAME'
      NEXT INSTRUCTION
```

The value of "M" must be odd, in order to make the next instruction properly aligned. An alternate approach used the convention on register 15:

```
NAME   CSECT
USING NAME,R15
      B      NEXTINST
      DC    X'M'
      DC    CLM'NAME'
NEXTINST EQU   *
```

MVT, MVS and OS/390 follow these conventions strongly. In particular, the system often destroys the contents of registers 0, 1, 14, and 15 when it returns control from a system MACRO, an SVC, or another system function. One must save the contents of these registers before execution one of these functions (hard to locate errors will frequently occur after failure to do so).

It is a good idea to mark a save area upon exit. This is usually done by moving X'FF" into the first byte of the fourth word of the save area (the place register 14 was stored). Although this technique does not seriously affect the contents of the save area for reading a dump, this technique quickly shows what save areas are active and which are not active when reading a dump. This technique does not work in OS/390 when the address mode is 31-bits.

Register 13 must be kept as the save area pointer; however, by careful programming, it can also double as a base register. You may set up your own save area for this purpose by setting it high in the program, and following it by a using on register 13, referencing the name of the save area. For reserving the 16 full words on storage for a save area, us DC instead of DS. A constant of F'0', or F'-1' will quickly show in a dump if the save area was ever used.

SAVE and RETURN are two system MACROS which will eliminate most of the coding for saving and returning conventions. SAVE generates the code necessary to save a specified series of registers. The registers are specified as they would be for a STM instruction. In addition, the operand "T" will cause registers 14 and 15 to be stored, regardless of what other registers may also be saved from the pair specified. The following example will cause registers 5,6, ..., 10,14, and 15 to be saved.

Save (R5,R10),T

The RETURN MACRO will generate code to restore registers, insert a return code in register 15, flag the save area (X'FF" in full word 4), and branch back via register 14. The registers to be restored are coded as with save. If register 15 already has a return code in it and should not be restored, it is coded as RC=(R15); else RC=N may be coded, where N is some value to insert into register 15. The operand "T" causes the flag X'FF" to be inserted in the save area. The following code will restore registers 5,6, ..., 10 to be reloaded, the save area to be flagged, and register 15 to be loaded with a value of 16.

RETURN (R5,R10),T,RC=16

NOTE both of these MACROS expect that register 13 will already be loaded with the address of the appropriate save area.

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- Mainframe Week - Code mvs
 - DS 0H WTO `PCAUTHPG-E003 Unable to get a system linkage LX' B ... Set up exit requirements ** Load save area address and free savearea ** Restore caller's ARs ... www.mainframeweek.com/code/showcode.php/0010/mw10mvs1.txt [cached](#) | [more results from this site](#)
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- len ... search390.tsctarget.com/fip/1,289483,sid10_gd535831,00.html [cached](#)
- Understanding and Debugging Kernel Panics
 - The initial stack pointer is the value of GPR1 in the savearea . The value of LR from the linkage area of the stack frame is printed, then the next stack frame ... developer.apple.com/technotes/tn2002/tn2063.html [cached](#) | [more results from this site](#)
- CICS Debugging and Dump Analysis Mainframe Applications Course -
 - ... SAVEAREA's; Return Codes; Module ... Module 10 LOCATING LINKAGE SECTION VARIABLES AND INDEXES; Linkage Sections; Task Global Tables; Indexes. Module ... www.datatrain.nl/training_mvs_os390_cics_debugging_and_dump_analysis.htm [cached](#)
- Communication with Assembler Programs : Calling aC Program from ...
 - ... Calling aC main Function from Assembler via \$MAIN0 MAINASM CSECT STM 14,12,12(13) standard OS entry linkage BALR 9,0 USING *.LR 14,13 LA 13,SAVEAREA ST 14,4 ... support.sas.com/documentation/onlinedoc/sasc/doc/clug/zasscp.htm [cached](#)

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Standard member-to-member linkage is used. ... WTO " ASMASMA1 calling ASMASMAA without parameters... LA R13,SAVEAREA SR R1,R1 CALL www.simotone.com/asmasma01.htm [cached](#)

- SAS/C SAMPLE: LOADMAA - C main dynamically loading an assembler ...
 - Standard OS Entry Linkage * - STM 14 ... 13 Save callee save area addr LA 13,SAVEAREA Put mine ... ftp.sas.com/techsup/download/SASC/sample/asm/LOADMAA.html [cached](#) | [more results from this site](#)
- Zee Guide to z/Architecture Zee Guide to z/Architecture (PDF)
 - ASCE (DAT) CR 7: Secondary ASCE (DAT) CR10: PER Range Start CR11: PER Range End CR12: Trace Entry Address CR13: Home ASCE (DAT) CR15: Linkage Stack Control ... docs.share.org/proceedings/sh100/h2818.pdf [view as html](#)
- <http://www.geocities.com/SiliconValley/Vista/9167/Sources/Loadm001.txt>
 - W_CLEN(2),D\$CESDLN CALL @OS\$6HC,(W_CLEN,OSCLEN,2) BR R11 * - Linkage Editor Data ... LV=GETM_L2,A=GETM_A2 FREEMAIN RC,LV=GETM_L_A=GETM_A_L R13,SAVEAREA+4 RETURN ... www.geocities.com/SiliconValley/Vista/9167/Sources/Loadm001.txt [cached](#) | [more results from this site](#)
- Seminare (PDF)
 - Hilfe der Dump Analyse - Debugger * Linkage Konventionen Parameterübergabe - Linkage Editor Optionen - Registerkonventionen - Savearea Verkettung - Reentrant ... www.cross-systems.de/pdf/lbm-sprachen.pdf
- Assembler Coding in the z/OS Environment: Part I (PDF)
 - F1SA * Your routine LA R0,3 Say what we want to get back from the linkage stack ESTA R0 ... After the BSM, you will replace R14 (31-bit version) in the savearea ... www.naspa.com/PDF/2002/202%20PDF/T0202004.pdf [more results from this site](#)
- Horrible Things You Can Do With IDCAMS!
 - Lastly, SAVEAREA+4 contains C savearea address so that callback can directly return to caller. ... pragma linkage(myidcams,OS), #include <stdio.h> ... www.multi-platforms.com/Tips/HombleIDCAMS1.htm [cached](#)
- CICS Program Debugging and Dump Analysis
 - Understand IBM's Standard Linkage Convention and the use of registers, the base and displacement addressing mechanism, and register savearea chaining. ... www.netg.com/catalog/detail.asp?c=US13748 [cached](#)
- <http://www.itc.virginia.edu/~drs8h/articles/lrupd1.txt>
 - 12 R13 EQU 13 R14 EQU 14 R15 EQU 15 OPENLRS CSECT STM R14,R12,12(R13) ADDRESSABILITY USING OPENLRS,R12 AND STANDARD LINKAGE LR R12,R15 LA R11,SAVEAREA ST R13,4 ... www.itc.virginia.edu/~drs8h/articles/lrupd1.txt [cached](#)
- Tips & Newsletters-Search390.com, a TechTarget Web site
 - Lastly, SAVEAREA+4 contains C savearea address so that callback can directly return to caller. ... pragma linkage(myidcams,OS), #include struct {void *addr; int

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1. [Introduction To Programming In An IBM Environment \(PDF\)](#) ...
... Tasks And Transactions Resource Definition File System Definition File Program ... Understand IBM's Standard ... Addressing mechanism, and Register Savearea chaining ...
www.dix.com/pdfl/dataltrainpdfs/intro.pdf [view as html](#)
2. [CPICS-I PTF06](#) ...
... THIS APAR CORRECTS A SOC4 ABEND IN DKNDIXZD DUE TO SAVEAREA AND AMODE PROBLEMS ... IBM, OW43357, ... THESE BITS WERE SET BASED ON THE MDS FIELD DEFINITION RATHER THAN THE ...
www.scl-mail.com/guru2067.htm [cached](#)
3. [TCP/IP for VSE/ESA IBM Program Setup and Supplementary\(PDF\)](#) ...
... 218 iv TCP/IP for VSE/ESA IBM Program Setup and Supplementary Information Page 7. GSKSSOC CLOSE 359 Transient Data Definition ...
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4. [Xephon Archives](#) ...
... by scanning the VTAM Resource Definition Table (RDT) ... nameless individuals on IBM's Dial4IBM Question service ... R15 ->ENTRY POINT LA R13,SAVEAREA ->SAVEAREA MVC 4 ...
www.xephon.com/arcinframe.php/n012a04 [cached](#) | [more results from this site](#)
5. [Reading PL/X in VPL Reading PL/X in VPL Part 1 Part 1 Session..., \(PDF\)](#) ...
... See <http://w3-3.ibm.com/legal> ... defining block Unless nested block contains a definition of the ... instructions have equivalent Bits LM(R0,R4,Savearea); COS (OK_Value ...
www.share.org/proceedings/sh99/Sshare/data/S2863a.pdf [view as html](#) | [more results from this site](#)
6. [PC Routines for Dummies \(PDF\)](#) ...
... terminology not IBM's I Most IBM service routines ... Linkage Stack A system-managed savearea Populated by ... Table Entry: Individual PC Routine definition ! Entry ...
www.cs.niu.edu/csci/567/ho/prtn.pdf [view as html](#) | [more results from this site](#)
7. [Ulrich Weigand - \[3.0.1 PATCH\] S390 important bugfixes](#) ...
... 2001-08-10 Ulrich Weigand <uweigand@de.ibm.com> configb390ibm390 ...
TYPE_MODE (type) == SCodeMode / Mode of stack savearea. ... 771,780 — / The definition of this ...
gcc.gnu.org/ml/gcc-patches/2001-08/msg00616.html [cached](#) | [more results from this site](#)
8. [Microsoft PowerPoint - SE 313A - Unit 5 SSM 2K2-X-06.ppt \(PDF\)](#) ...

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15. <http://os390-mvs.hypermart.net/xephon/m092a08.txt> ...
... do dev1 = dev1 Model '_Jdn' / example : IBM 3390-9 / end ... IRXARGTB IRXEVALB IGDSGD, STORAGE GROUP DEFINITION IEFSSSA , SSOB ... R12 LA R2,SAVE MY SAVEAREA ST R13 ...
os390-mvs.hypermart.net/xephon/m092a08.txt [cached](#)
16. [DUMPSCAN INSPECT Help Information](#) ...
(c) Copyright IBM Corporation 1990 ... This requires: o Block Definition File (BDF) containing CP control ... Registers SAVBK Chain SAVBK at address SAVEAREA at address ...
www.ucc.uconn.edu/cgi-bin/crmshelp?DUMPSCAN%20INSPECT [cached](#)
17. [Assembly Language Manual Chapter 7](#) ...
... Notice that the fields of a structure definition may have ... 3. The IBM System/370 does not have a ... stored in a special memory area typically called the SAVEAREA.

<http://search.yahoo.com/bin/search?p=IBM+savearea+definition&ei=UTF-8>

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members.cruzio.com/~bluejay/chap7.html [cached](#)

18. [CICSplex SM Messages and Codes \(PDF\)](#) ...
Page 1. CICS Transaction Server for OS/390 IBM CICSplex SM Messages and Codes Release 3 GC33-0790-04 Page 2. Page 3. 185 Copyright IBM Corp. 1994, 1999 iii ...
examples.oreilly.com/cics/CDROM/pdfs/cicsts/eyua100.pdf [view as html](#)
19. [SPI Release 4.3 User's Guide and Reference](#) ...
... more information on multiple job exits, refer to the IBM VSE/ESA ... AR SPI308I SPI Savearea is at Xnnnnnnn ... A Date Definition Phrase is used to define holidays and ...
www.spurtsuits.com/mainframe/api/spi43usersguide.htm [cached](#)
20. [2003 Newsgroup postings \(3/31 - 4/8\) Lynn Wheeler](#) ...
... for allocation that allowed timer control definition on a ... Architecture of the IBM System/360 IBM Journal of ... could effectively get by with a static savearea. ...
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2. [OS/390 C/C++: XPLink, The Details \(PDF\)](#) ... Stack Layout Detail for future use Current Register 4 possibly in ... Entry Point Address (optional) Environment (optional) Backchain (optional) Savearea (48 bytes) ... [www.share.org/proceedings/sh98/data/S8121.PDF view as html](#) | more results from this site
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4. [Mainframe Week - Code mvs](#) ... 8(R13) LA R13,SAVEAREA * MAIN LINE ... ESTABLISH CALLING PANEL LAYOUT * DETERMIN NUMBER ... [www.mainframeweek.com/code/showcode.php/0048/mw48mvs3.txt cached](#) | more results from this site
5. [CICS Debugging and Dump Analysis Mainframe Applications Course](#) ... Operating System "Snap" Dump; Dump Layout. Module 4 LOCATING CICS AREAS OF INTEREST: CICS Areas Useful for Debugging; CWA Length; ... SAVEAREA's; Return Codes ... [www.datatrain.nl/training_mvs_os390_cics_debugging_and_dump_analysis.htm cached](#)
6. [Introduction To Programming In An IBM Environment \(PDF\)](#) ... Course, the student will be able to ... Understand the layout of a ... Registers, the Base and Displacement Addressing mechanism, and Register Savearea chaining. ... [www.dld.com/pdf/datatrainpdts/intro.pdf view as html](#)
7. [Migrating VSE System Applications to the Web: Part II - Porting ... \(PDF\)](#) ... R1 = Buffer address for returning the data * R2 = ACBLOK pointer * The ACBLOK has a layout as follows ... * Incoming:R2=ACBLOK address * R13=Savearea * R15=Base ... [www.naspa.com/PDF/2000/0200%20TS%20PDFs/T0002003.pdf more results from](#)

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- ... AMODE 31 DFSAOE00 RMODE ANY ** INITIALISATION CODE * USING SAVEAREA,R13 SAVE (14 ... RESERVED SPACE DYNLNG EQU -DYNWRK ** INPUT SEGMENT LAYOUT * SEGMIN DSECT DS ... [web.ukonline.co.uk/dougie.lawson/gse_files/dsaoe00.txt cached](#)
18. [Assembler to C Migration using the FermaT Transformation System \(PDF\)](#) ... Implemented in C, or to meet customers' requirements on the C layout or features ... On the other hand, if savearea is declared as an array of fullwords and the ... [www.smitd.com/icsm99-t.pdf view as html](#)
19. <http://www.planetmvs.com/unxmit/file005.txt> ... OF RECORD 00000660 SPACE 3 00000670 COPYR3 DSECT , RECORD 3-N LAYOUT 00000680 R3F ... USING IECOPY,R12,R11 00001280 SPACE ,00001280 LA R15,SAVEAREA 00001300 ST ... [www.planetmvs.com/unxmit/file005.txt cached](#)
20. [Re: \[SMAUG\] Building problem \(Seg Fault\)](#) ... > After spending a couple of hours throwing together the basic layout of my ... alterations but only method replacements in act_info.o I did > savearea and then ... [www.shadow-lands.com/sml/2001/msg03481.html cached](#)

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8. <http://www.iuassn.com/UCL/viewdb01.txt> ... DS OCL7 PARMRY DS CL4 PARMRLRD DS CL3 PARMHALC DS CL3 PARMNOEX DS CL2 PARMNOSP DS CL16 DS CL88 VS24BIT DSECT SAVEAREA DS 18F DWORD ... 008200 01 Header-Layout. ... [www.iuassn.com/UCL/viewdb01.txt cached](#)
9. [Henne's SMAUG Building Pages - Building Rooms](#) ... Remember that most important command: SAVEAREA ... doing any building, a Creator level Immortal should make a basic outline of their intended area layout on paper ... [webhome.idirect.com/~helppawn/smaug/olc-rooms.html cached](#)
10. [Condition Handling with Language Environment \(PDF\)](#) ... This 12-byte code is the same layout as the feedback code returned from ... Assembler it is no longer appropriate to have the traditional 18F savearea, and you no ... [www.ibm.com/servers/eserver/zseries/zos/e-conference/pdf/sm8234.pdf view as html](#) | more results from this site
11. [Finalist \(PDF\)](#) ... automatically initialized. Check the reicode field prior to the errinfo field. Errinfo has the layout shown next. Physical Length ... [umrnmvb.missouri.edu/pb/final/CHAPTR10.PDF view as html](#) | more results from this site
12. [Excalibur.. It's time to prove yourself](#) ... level immortal should make a basic outline of their intended area layout on paper ... Most importantly, be sure to SAVEAREA after you make any additions or changes ... [excalibur.sourceforge.net/docs/getdoc.php?docid=4 cached](#)
13. [Finalist \(PDF\)](#) ... Errinfo has the layout shown next. ... 30 27-56 Reserved for future expansion. Retinfo has the following layout for this function: Physical Length FromTo Position [www.state.ak.us/local/vkpages/ADMIN/info/mpldf/m/batch/chptr10.pdf view as html](#)
14. <http://www.kehei.com/public/archive/wiki0125.pl> ... ENV(SAVEAREA) : " . /exit" .. FILE, "> \$name" ; print FILE \$text; close(FILE); } # templates can have per-db layout sub readTemplate ... [www.kehei.com/public/archive/wiki0125.pl cached](#)
15. [Assembler to C Migration using the FermaT Transformation System \(PDF\)](#) ... implemented in C, or to meet customers' requirements on the C layout or features ... On the other hand, if savearea is declared as an array of fullwords and the ... [www.dur.ac.uk/martin.ward/martin/papers/icsm99-t.pdf view as html](#)
16. [Xephon Archives](#) ... DC X'08' CL'8' VP'013010' ACBTEND DC X'00' 8'FF' LTORG *** COMMAREA LAYOUT COMMAREA DSECT CMD0 ... SAVE (14,12),* LR R12,R15 USING VPPPO,R12 ST R13,SAVEAREA+4 LR ... [www.xephon.com/arcinframe.php/n019a02 cached](#) | more results from this site
17. http://web.ukonline.co.uk/dougie.lawson/gse_files/dfsaoe00.txt


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[savearea](#) ... I have done all everything you guys have suggested but everytime I try to [savearea](#) it says: Build: Raistlin: [savearea](#) Log: Saving Raistlin.are... ...
www.shadow-lands.com/sml/1997/msg05159.html [cached](#) | [more results from this site](#)

2. [\[EROS-Arch\] SaveArea.h](#)

[EROS-Arch] [SaveArea.h](#). Jonathan S. Shapiro ... [SaveArea.h](#) is a thin wrapper around fixregs.h, which is machine generated. Strictly speaking, you ...
www.eros-os.org/pipermail/eros-arch/2001-March/002854.html [cached](#) | [more results from this site](#)

3. [ClipX - Force 4.0 Reference - savearea\(\) save screen area](#)

... [savearea\(\)](#) Save screen area ---- Declaration screen.hdr Syntax proc [savearea](#) extern ...
www.clipx.net/ng/force40/ng12512f.php [cached](#) | [more results from this site](#)

4. http://www.geocities.com/SiliconValley/Vista/9167/Sources/os_6hc.txt

R03 ST R13,[SAVEAREA+4](#) Save A(OLD [SaveArea](#)) LR R12,R13 LA
R13,[SAVEAREA](#) ST R13,8(R12) Save A(NEW [SaveArea](#)) * --- from characher --- L
R07,0(R01) * --- to ...
www.geocities.com/SiliconValley/Vista/9167/Sources/os_6hc.txt [cached](#) | [more results from this site](#)

5. http://www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt (MICROSOFT POWERPOINT)

... Global Data (p. 123): for process management, IPC and disk I/O. Note [SaveArea](#), ProcessDescriptor, & interrupt vector pointers. ... struct [SaveArea](#) { ...
www.cs.fsu.edu/~xyuan/cop4610/ch5_2.ppt [view as html](#) | [more results from this site](#)

6. [savearea](#)

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homepage3.nifty.com/hm-fire20/savearea.htm [cached](#)

7. [Python: module rox.saving](#)

... Classes. [gtk.Dialog\(gtk.Window\)](#) [SaveBox](#) [StringSaver\(SaveBox, Saveable\)](#)

- exceptions.Exception AbortSave Saveable SaveFilter gtk.VBox(gtk.Box)
SaveArea ...
rox.sourceforge.net/rox_lib_docs/rox.saving.html [cached](#)
8. **BAL & BALR (MICROSOFT POWERPOINT)** ↗
... Example: STM R6,R10,SAVEAREA Save registers. . . . Wraps around if order seems backwards: STM R14,R1,SAVEAREA SAVEAREA DS 4F Holds R14, R15, R0, R1. 9.
www.math.uni.lodz.pl/~miodekk/OS/390/ppt/8subrtns.ppt [view as html](#) | [more results from this site](#)
9. **Gammon Software Solutions forum** ↗
... **savearea**. ... kay when I try to use **savearea** to save a newly assign area it says your area is not loaded I have a building directory so that's not the problem.
www.gammon.com.au/forum/bbshowpost.php?bbsubject_id=390 [cached](#) | [more results from this site](#)
10. **Lecture 1: Course Introduction and Overview (MICROSOFT POWERPOINT)** ↗
... / . sioint_stub: sw [**savearea**], R0. sw [**savearea+4**], R1. sw [**savearea+8**], R2. ; [. . . sw [**savearea+32**], oldPC. call sio_handler. lw oldPC, [**savearea+32**]. ; [. . .
www.cc.gatech.edu/classes/AY2003/cs2200_spring/Slides/Lec10-proc.ppt [view as html](#) | [more results from this site](#)
11. **Xephon Archives** ↗
... XA SP2.1). FINDSYMB ASSEMBLE TITLE 'FINDSYMB - Find CP symbol' PRINT GEN FINDSYMB CSECT DS 0H STM R14,R12,12(R13) * Save caller's regs in his **savearea** BALR R12 ...
www.xephon.com/arcinframe.php/v079a01 [cached](#) | [more results from this site](#)
12. **<http://www.cs.wpi.edu/~claypool/courses/502-Su03/samples/SOS/timer.c>** ↗
#include "system.h" /* This procedure is called when a timer interrupt occurs. */ void TimerInterruptHandler(void) { struct **SaveArea** ***savearea**; if ...
www.cs.wpi.edu/~claypool/courses/502-Su03/samples/SOS/timer.c [cached](#) | [more results from this site](#)
13. **Mainframe Week - Code racf** ↗
... RACFDGEN.&SYSDATE SAVE REGISTERS LR R12,R15 | LA R11,4095(R12) | INITIALIZATION OF BASE REGISTERS LA R11,1(R11) | LR R2,R13 ADDRESS OF PRECEDING **SAVEAREA** LA R13 ...
www.mainframeweek.com/code/showcode.php/0013/mw13racf1.txt [cached](#) | [more results from this site](#)
14. **[http://www.netlib.org/go\(ibmblas](http://www.netlib.org/go(ibmblas)** ↗
... PRINT NOGEN &NAME CSECT EQUATE AIF ('&TRACE' NE 'YES').L1 HSA EQU 4 . HIGHER **SAVEAREA** LSA EQU 8 . LOWER **SAVEAREA** .L1 ANOP &CALLQ SETB ('&TRACE' EQ 'YES' ...
[www.netlib.org/go\(ibmblas](http://www.netlib.org/go(ibmblas) [cached](#)
15. **[http://elib.zib.de/netlib/go\(ibmblas](http://elib.zib.de/netlib/go(ibmblas)** ↗
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[elib.zib.de/netlib/go\(ibmblas](http://elib.zib.de/netlib/go(ibmblas) [cached](#)
16. **Chapter 15 - A USING Instruction Tutorial (PDF)** ↗

... MAIN CSECT 2 STMINST STM 14,12,12(13) Save regs; note explicit base (13) 3
USING MAIN,15 R15 will have A(MAIN) at execution 4 LAINST LA 14,SAVEAREA
Get address ...
www.cs.niu.edu/~mstack/csci/using.pdf [view as html](#) | [more results from this site](#)

17. Horrible Things You Can Do With IDCAMS! ↗

... Lastly, **SAVEAREA+4** contains C **savearea** address so that callback can directly return to caller. ... ST R7,BASEADDR Save base. ST R13,**SAVEAREA+4** Backward pointer. ...
www.multi-platforms.com/Tips/HorribleIDCAMS1.htm [cached](#)

18. 22C:116, Homework 5 Solutions, Fall 2001 ↗

... cause not one of **addr_fault** or **access_fault**, this is not a page fault, do other things not related to this assignment else **page = pagefield(savearea.va)** if ...
www.cs.uiowa.edu/~jones/opsys/fall01/hw/05sol.html [cached](#)

19. Tips & Newsletters-Search390.com, a TechTarget Web site ↗

... routines can access. Lastly, **SAVEAREA+4** contains C **savearea** address so that callback can directly return to caller. The example ...
search390.techtarget.com/tip/1,289483,sid10_gci535831,00.html [cached](#) | [more results from this site](#)

20. <http://members.tripod.co.uk/nvcs/Samples/nvcsget.htm> ↗

... EQU 12 R13 EQU 13 R14 EQU 14 R15 EQU 15 EJECT STM R14,R12,12(R13) *
SAVE ENTRY REGS BALR R12,R0 * ESTABLISH BASE USING *,R12 LR R2,R13 *
CALLER **SAVEAREA** LA R13 ...
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1. Sketch of S/360 Subroutines – Mark Smotherman [PDF](#)
... This mainframe architecture lives on today in the IBM zSeries architecture (having been ... pseudo-op to tell assembler to use R12 st r13,savearea+4 I store ...
www.cs.clemson.edu/~mark/subroutines/s360.html cached | more results from this site
2. IBM Linux Technology Center , Open Source Patches [PDF](#)
... giveup_fpu will get a pointer to our (current's) last register savearea via current->thread regs and using that pointer will turn off the MSR_FP, MSR_FEO and ...
oss.software.ibm.com/linux/patches/?patch_id=169 cached
3. Re: Porting Arta to Linux/390 [PDF](#)
... savearea "context"; And the savearea struct is actually called lwp_context these days ... Johan: References: Porting Arta to Linux/390: From: mayding@us.ibm.com. Re ...
www.stacken.kth.se/lists/arta-drinkers/2000-07/msg00035.html cached | more results from this site
4. IBM COBOL for MVS & VM Version 1 Release 2 Performance Tuning ... [PDF](#)
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... Understand IBM's Standard Linkage Convention and the use of Registers, the Base and Displacement Addressing mechanism, and Register Savearea chaining. ...
www.did.com/pd/pdatrainpdfs/intro.pdf view as html
6. <http://www.wiu.edu/users/mfill/cs310/unit76.html> [PDF](#)
... The template for a subroutine obeying the IBM-convention is: subname equ * STM 14 ... area allocated by the subroutine subroutine call template LA 13,savearea LA 15 ...
www.wiu.edu/users/mfill/cs310/unit76.html cached
7. CPCSGURU - IBM supplied Usermods for CPCS PTF22 [PDF](#)
... IBM Provided "Usermods" for CPCS PTF22. ... level of PQ38827 This USERMOD corrects several abend codes which could occur due to a corrupted savearea address when ...
www.sci-mail.com/guru2227.htm cached | more results from this site
8. The Future z/OS Application: Virtually an Endless Space [PDF](#)
... Page 8. zSeries IBM New Savearea Formats F2SA - reserved format code F3SA - reserved format code F4SA - 144-byte savearea; caller's registers saved in ...

<http://search.yahoo.com/bin/search?p=IBM+savearea&ei=UTF-8>
Yahoo! Search Results for IBM savearea

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18. Mainframe Week - Code db2 [PDF](#)
... PILLAY ** FOR MORE INFORMATION...: DB2 ADMIN GUIDE ** APAR INFO : WWW.SOFTWARE.IBM.COM/DATA ... ENTRY POINT USING I258,R12 USE R4 AS BASE XC SAVEAREA(72),SAVEAREA ...
www.mainframeweek.com/code/showcode.php/0015/mw15db23.txt cached | more results from this site
19. Controle van het besturingsverloop (MICROSOFT POWERPOINT) [PDF](#)
... adr(M), adr(I), ..., K, L, M, I, Procedure-oproep op IBM 380: alternatief, LA 13,SAVEAREA, L 15,ADR\$OM, LA 14,RETOUR, BALR 1,15, ARGLIST DC A(K), DC A(L), DC A(M), DC A(I), ...
www.ells.ug.ac.be/~kdb/cai/h5.ppt view as html
20. WRP60 [PDF](#)
... In MVS, Register 13 is the savearea pointer. ... ===== Newsgroups:
bit.listserv.ibm-main From: edjaiffe@netcom ...
home.xnet.com/~msk/futuresource/hamasaki/wrp60.html cached

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9. Assembler Coding in the z/OS Environment: Part I [PDF](#)
... After the BSM, you will replace R14 (31-bit version) in the savearea. The IBM RETURN macro is also replaced with code that matches the RETURN macro except in ...
www.naspa.com/PDF/2002/2020%20PDF/T0202004.pdf more results from this site
10. Assembler Call, Load and Link [PDF](#)
... This program will run an an IBM Mainframe using MVS or a PC ... WTO ~ ASMASMA1 calling ASMASMAA without parameters...` LA R13,SAVEAREA SR R1,R1 CALL ASMASMAA ...
www.simotime.com/asmasm01.htm cached
11. http://openbsd.secstrup.org/src/usr.sbin/afs/src/lwp/lwp_asm.c [PDF](#)
... ALL ** IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS, IN NO EVENT SHALL IBM ** BE LIABLE ... savecontext(int (*ep)(), struct lwp_context *savearea, char *sp ...
openbsd.secstrup.org/src/usr.sbin/afs/src/lwp/lwp_asm.c cached
12. PC Routines for Dummies [PDF](#)
... Space Switching Global: I My terminology not IBM's ! Most IBM service routines ... 5 PC Routines for Dummies Linkage Stack A system-managed savearea Populated by ...
www.cs.niu.edu/csci/567/ho/portn.pdf view as html | more results from this site
13. Dave's S/390® Assembler FAQ page [PDF](#)
... savearea Bytes 138-143: address of next savearea This entry was taken from a post by Peter Nelson (z/OS Core Technology Design) on the IBM-Main newsgroup ...
www.planetrms.com/hlasmv390faq.html cached | more results from this site
14. <http://www.freueusp.org/Arco/Data/sparc/r4to2> [PDF](#)
... 00260000 `A 00270000 `A 00280000 `A CONVERT 32 BIT IBM FLOATING POINT VALUES TO ... 00780000 STM R14,R12,12(R13) SAVE REGISTERS 00790000 LA R12,SAVEAREA POINTER TO ...
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15. Zee Guide to z/Architecture Zee Guide to z/Architecture [PDF](#)
... for zArchitecture Designed by ASCE bit 58 (R) Does not undergo address translation: The virtual addresses treated as real DAT tables not used IBM calls this ...
docs.share.org/proceedings/sh100/s2818.pdf view as html
16. LTC bulletin: May 21, 2001 [PDF](#)
... giveup_fpu got a pointer to the (current's) last register savearea via current->thread ... Maneesh has been working with the IBM Software Lab, India since December ...
www-105.ibm.com/developerworks/linux/library/l-ltc3.html cached
17. Xophon Archives [PDF](#)
... My thanks to the nameless individuals on IBM's Dia-IBM Question service and to Bill Earls ... FOR PROGRAM LR R12,R15 ->ENTRY POINT LA R13,SAVEAREA ->SAVEAREA MVC 4 ...
www.xophon.com/varcinfoframe.php/n012a04 cached | more results from this site

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 - ... DEPENDING ON SPLIT SCREEN) IF THE REXX CONTAINING THE '>>>' SELECT PGM IS ... STORAGE USING PGMAREA,R11 ESTABLISH DATA AREA ADDRESSABILITY ST R13,SAVEAREA+4 ,SAVE ... www.mainframeweek.com/code/showcode.php?0015/mv15mvs2.txt [cached](#) | [more results from this site](#)
2. [Xephon Archives](#)
 - ... SELECT ESDS-FILE ASSIGN TO AS-HMZNDIAG ORGANIZATION IS SEQUENTIAL. SELECT RBA-FILE ASSIGN TO HMZNPROC. ... 05 A-SAVEAREA-COUNT PIC S9(4) COMP SYNC, 01 CONSTANTS. ... www.xephon.com/armframe.php?012a02 [cached](#) | [more results from this site](#)
3. [IOF Q&A Message Board](#)
 - ... &STR0 THEN DO :& BROWSE :ENDO :ELSE DO :& SELECT :EXCL DDNAME NE &ADDNAME :1 SELECT :ENDO :FIND ... SPACE ,: LA R1,WRKSAVE Chain new savearea to : ST ... www.triangle-systems.com/wwwboard/messages/484.shtml [cached](#)
4. [Chapter 15 - A USING Instruction Tutorial \(PDF\)](#)
 - ... has only one USING available for each instruction from which to select a base ... this program, particularly how it handles: 4 LAINST LA 14,SAVEAREA Get address ... www.cs.niu.edu/~mstack/csci/using.pdf [view as html](#)
5. [COPYKEYS: An ISPF Solution \(PDF\)](#)
 - ... Upon return from the SELECT, the table is again TBDISPL'd, this time with ... Stor Address-< R10 '/USING STORAGE,R10 /' Base Storage '/ ST R13,SAVEAREA+4 /' Set ... www.nspa.com/PDF/89/T9910003.pdf
6. <http://www.sillysoft.com/ftp/editu.txt>
 - ... a). Signal out End End Address two 'ALLOC F(A1) DA(indataset) REU SHR' 'SELECT PGMT(C ... GETMAIN RU,LV=(0),SP=0,LOC=BELOW ST 1,8(13) SAVE SAVEAREA ADDR IN ... www.sillysoft.com/ftp/editu.txt [cached](#) | [more results from this site](#)
7. [06-01_shoshiki](#)
 - ... PasteSpecial xlPasteFormats .Interior.ColorIndex = 8 End With Target.Select Set PrevCell = ActiveCell End If Target.Select Set SaveArea = Nothing .CutCopyMode ... www.h2.aihai.ne.jp/~naka95/Excel_Note/06-01_shoshiki.htm [cached](#)
8. [SuperPaint Plug-in](#)

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www.clipx.net/ng/force40/ng9f108.php [cached](#)

18. [Reading PL/X in VPL Reading PL/X in VPL Part 1 Part 1 Session ... \(PDF\)](#)
 - ... path, or a DO/SELECT Decrementated at end of path, or on END of DO/SELECT ... set (over 160) of instructions have equivalent Bits LM(R0,R4,Savearea); CDS (Old_Value ... www.share.org/proceedings/sh99/Sshare/data/S2863a.pdf [view as html](#)
19. [Ausam/sys/conf/power.s-new](#)
 - ... 2,ap / get a one word temp stack / mov \$rest,mover / select desired mov ... volatile locations described by 'tosave' copies: .if .START -1 mov \$savearea,r1 / save ... minnie.tuhs.org/UnixTree/Ausam/sys/conf/power.s-new.html [cached](#)
20. <http://www.iuassn.com/UCL/viewdb01.txt>
 - ... DS CL3 PARMINEX DS CL2 PARMINOSP DS CL16 DS CL98 VS24BIT DSECT SAVEAREA DS 18F ... 001300 Select Sys001-File Assign To Sys001 001400 File Status is Sys001-Status. ... www.iuassn.com/UCL/viewdb01.txt [cached](#)

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... Go back to the drop window, select the entire window, and command Capture ...
ForgeMask = 8; { discard all mask and saved } { data } type SaveArea = record
{ area ...
www.mactech.com/articles/mactech/Vol.08/08.01/SuperPaintPlug-in_cached.html

9. [Z4Select \(PDF\)](#)
 - ... OUTREC B MAINLOOP WRTSEL C DS 0H PUT SELECT,OUTREC B MAINLOOP RETURN DS 0H MV1 Z4FUNC,C9' BAL R7,CALLZ4 CLOSE FILEIN,SELECT,NOSELECT)' L R13,SAVEAREA+4 LM ... www.state.ak.us/local/kpages/ADMIN/Info/thpd/mz/z4select/chapt05.pdf [view as html](#) | [more results from this site](#)
10. [Bixoft - BXAO - Static glue module](#)
 - ... Set parameter version to 07 03700000 MVC LNSFILES,+C'00000010' * Select all files ... BR R14 * Return to mainline 03750000 * 03760000 EJECT 03770000 SAVEAREA DS 9D ... www.bixot.net/goods/bxao.htm [cached](#) | [more results from this site](#)
11. <http://os390-mvs.hypermart.net/xephon/m092a08.txt>
 - ... ucba) retc = length(lsp) if retc >= 38 then retc = 0 bx = ? perc = ??? select when retc ... SYSVTOC ENQ' BASR R12,0 USING *,R12 LA R2,SAVE MY SAVEAREA ST R13 ... os390-mvs.hypermart.net/xephon/m092a08.txt [cached](#)
12. [Sub ScoreBasedAllocation\(\) \(MICROSOFT WORD\)](#)
 - ... SolverSolve userFinish:=False, SolverSave saveArea:=Range("A100"), Range ("B2"), Offset(0, NumPort + 1).Copy, Range(Cells(2, 3), Cells(2, NumPort + 2)).Select ... faculty.fuqua.duke.edu/~charvey/Teaching/BA453_1999/LtcmCode.doc [view as html](#)
13. [Dave's S390® Assembler FAQ page](#)
 - ... The final three X20's are the rest of the digit select characters necessary for ... such as what to do if your caller provides a 72-byte savearea, and you need to ... www.planetmvs.com/hlasm/s390faq.html [cached](#)
14. http://www.geocities.com/SiliconValley/Vista/9167/Sources/rv_080.txt
 - ... CLI @SELECT,CR' BE @RESTORE CLI @SELECT,CA ... B @TERM8 --- TERM8 EQU 'L R13,SAVEAREA+4 RETURN ... www.geocities.com/SiliconValley/Vista/9167/Sources/rv_080.txt [cached](#)
15. [Using the 3494 Tape Library under VSE/ESA](#)
 - ... With a new EXTRACT service it is possible to select a free 3490E device in a ... R7,MYLIB LA R8,MYSERVL LA R9,WRITEFL LA R10,TCATEG LA R13,SAVEAREA USING IJABSER ... www.ibm.com/servers/eserver/zseries/os/vse/support/af/vsesup.htm [cached](#) | [more results from this site](#)
16. [Finalist \(PDF\)](#)
 - ... COMMENT Card 4-5 CTYCORG Card 4-6 FIELDS Card 4-6 INPUT Cards 4-10 OUTPUT Cards 4-10 OPTION Cards 4-11 OUTBLD Cards 4-14 PRINTMOD Card 4-15 SELECT/EXCLUD Card 4 ... umrwsb.missouri.edu/pb/final/CHAPTR04.PDF [view as html](#)
17. [ClipX - Force 4.0 Reference - pickdisp\(\) display a string list ...](#)
 - ... pSub, "Item &7") pickadd(pSub, "Item &8") pickadd(pSub, "Item &9") savearea (4, 31 ... otherwise nCh := Inkey) do case // We use Enter here to select an item ...

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